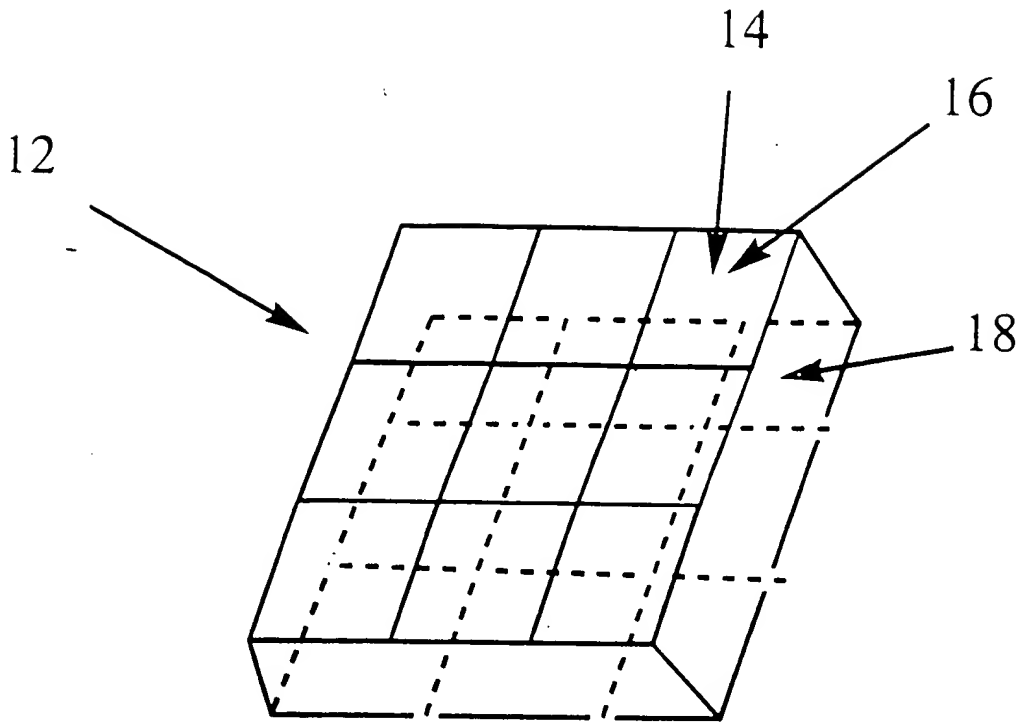
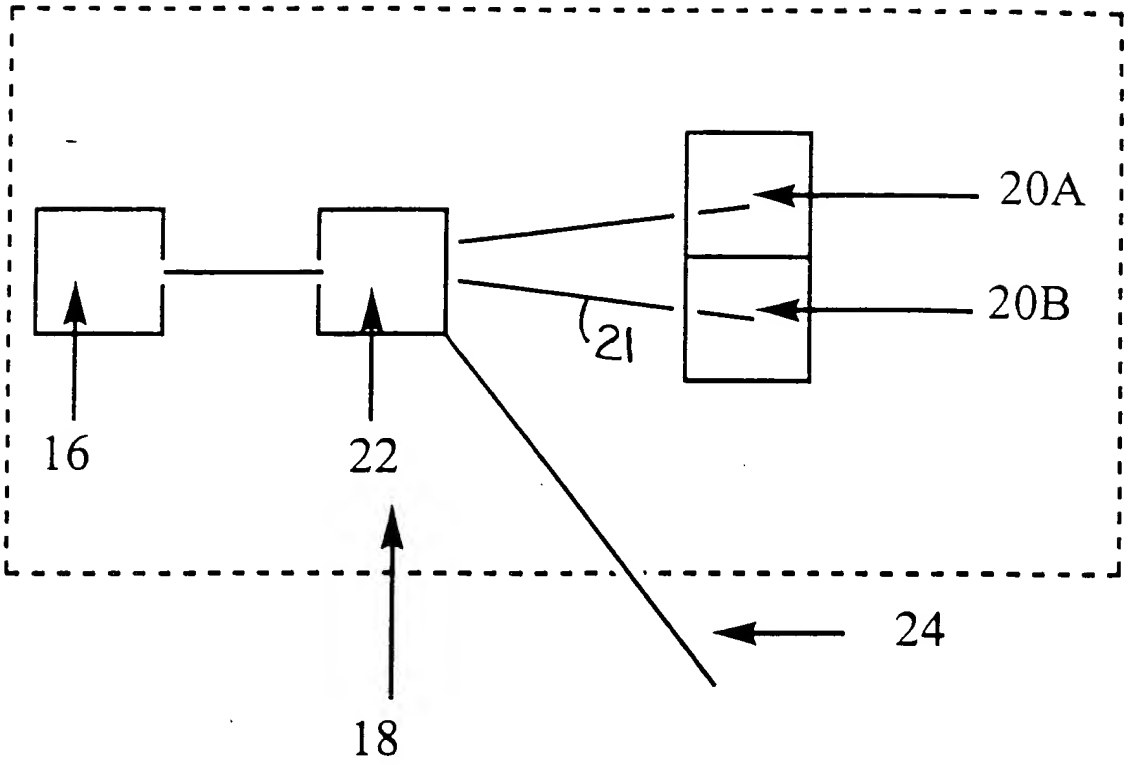
FIGURE 1



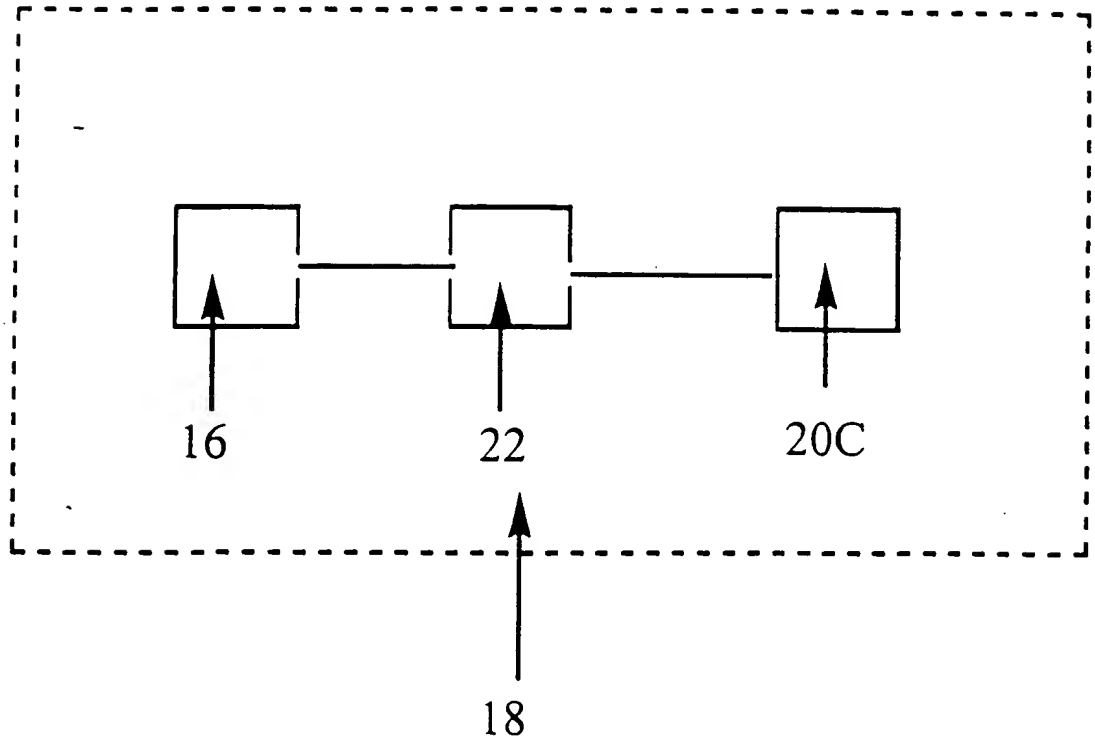
DOCKET: 6746.0

FIGURE 2



0054-2454-0900

FIGURE 3



09574890 - 1251400

FIGURE 4A

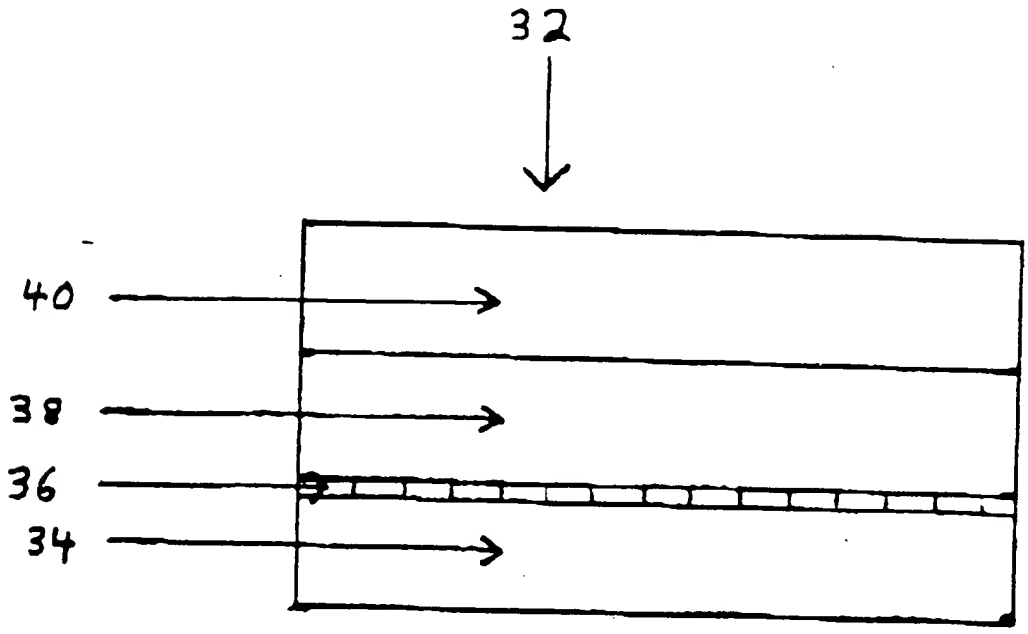
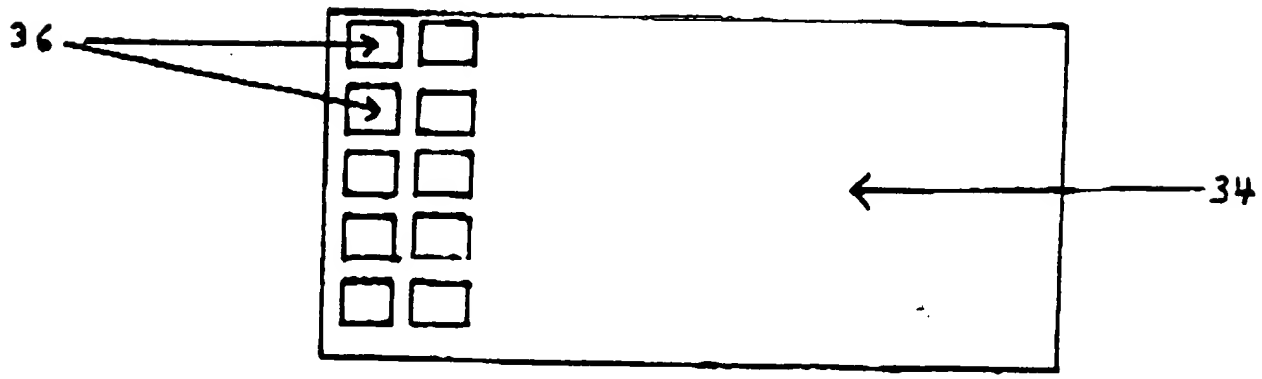
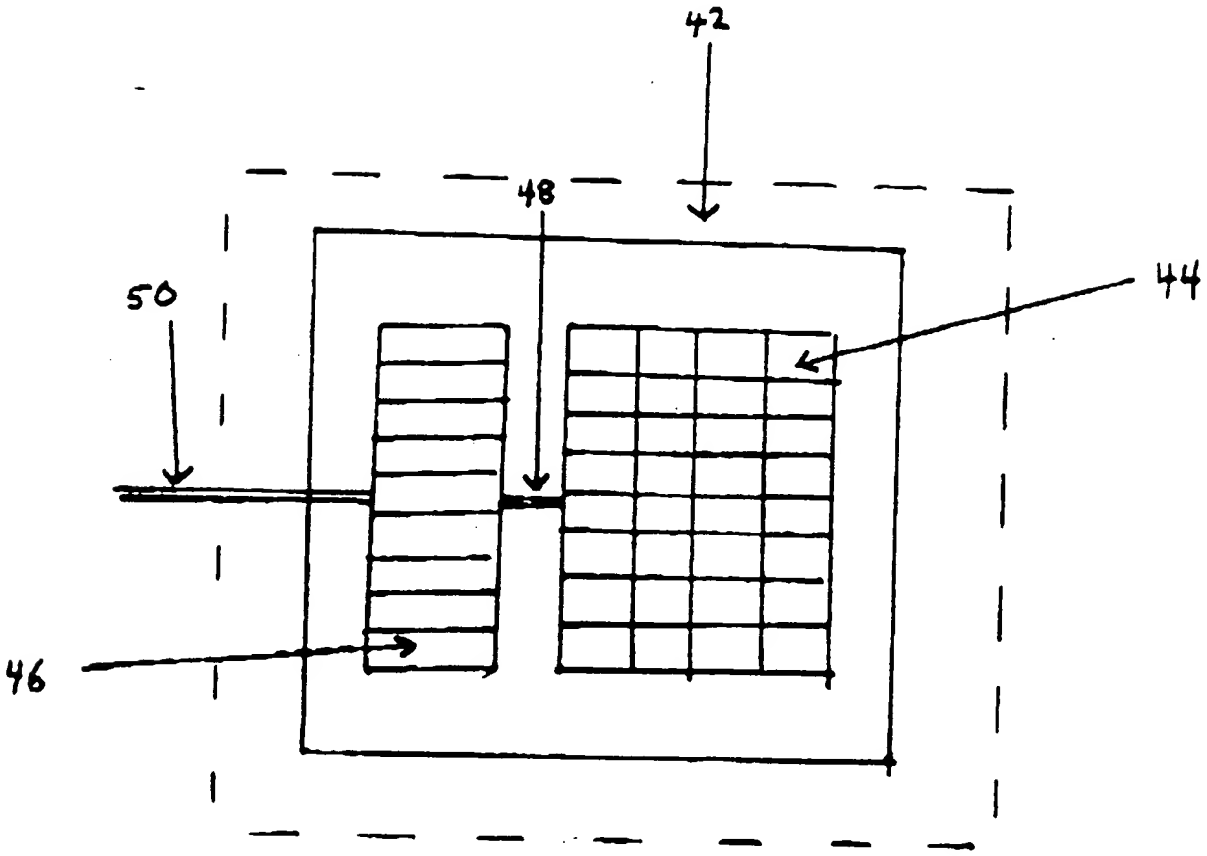


FIGURE 4B



DOCKET ALARM

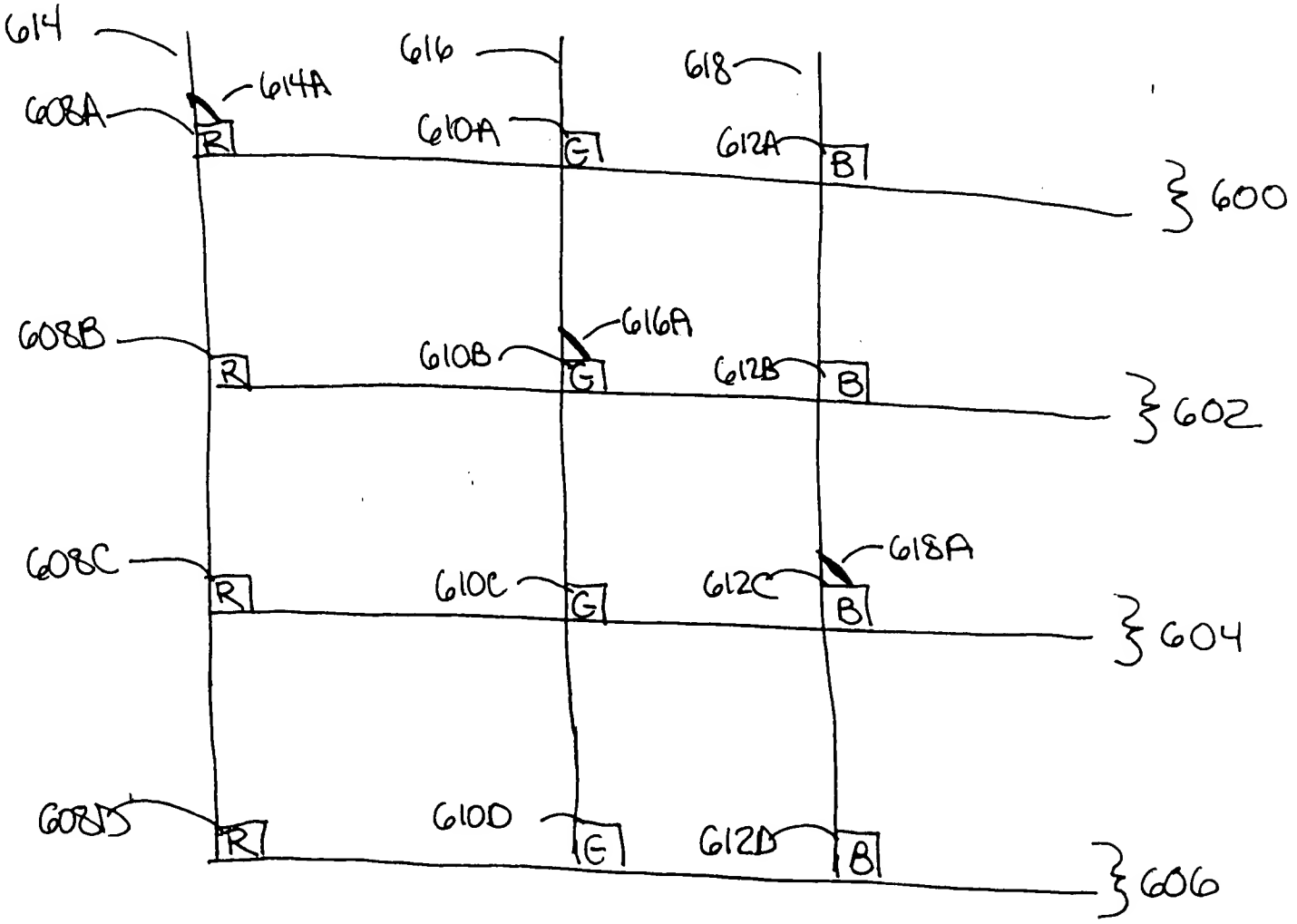
FIGURE 5



0049-1748-1750

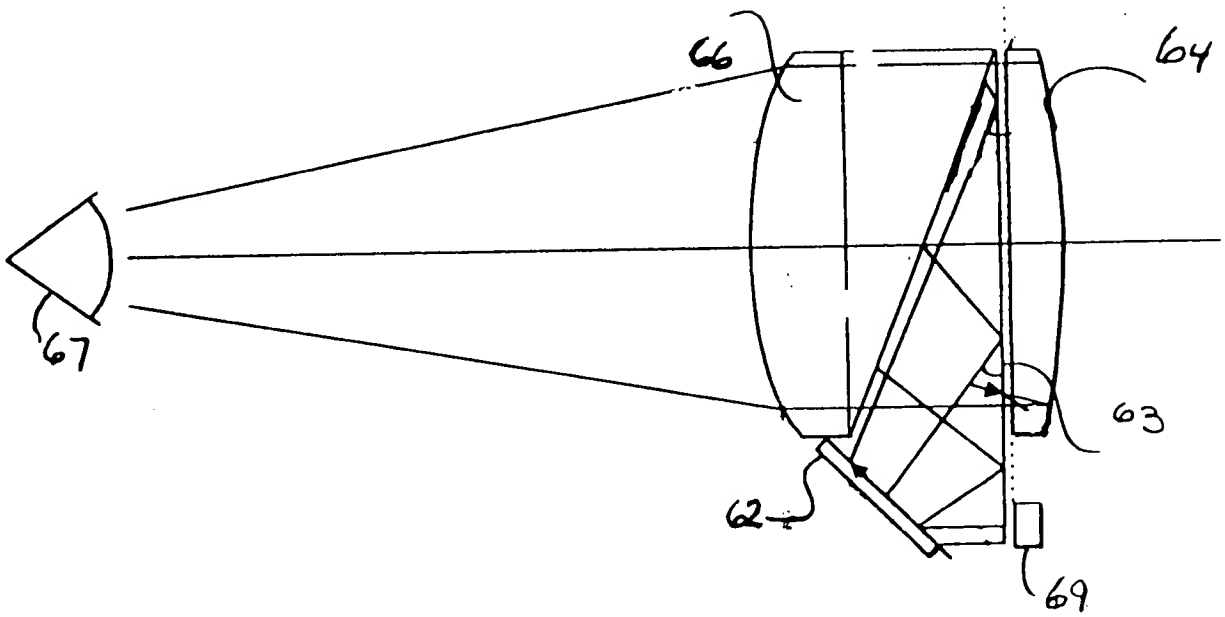
DOMEST" BTH2E260

FIGURE 6



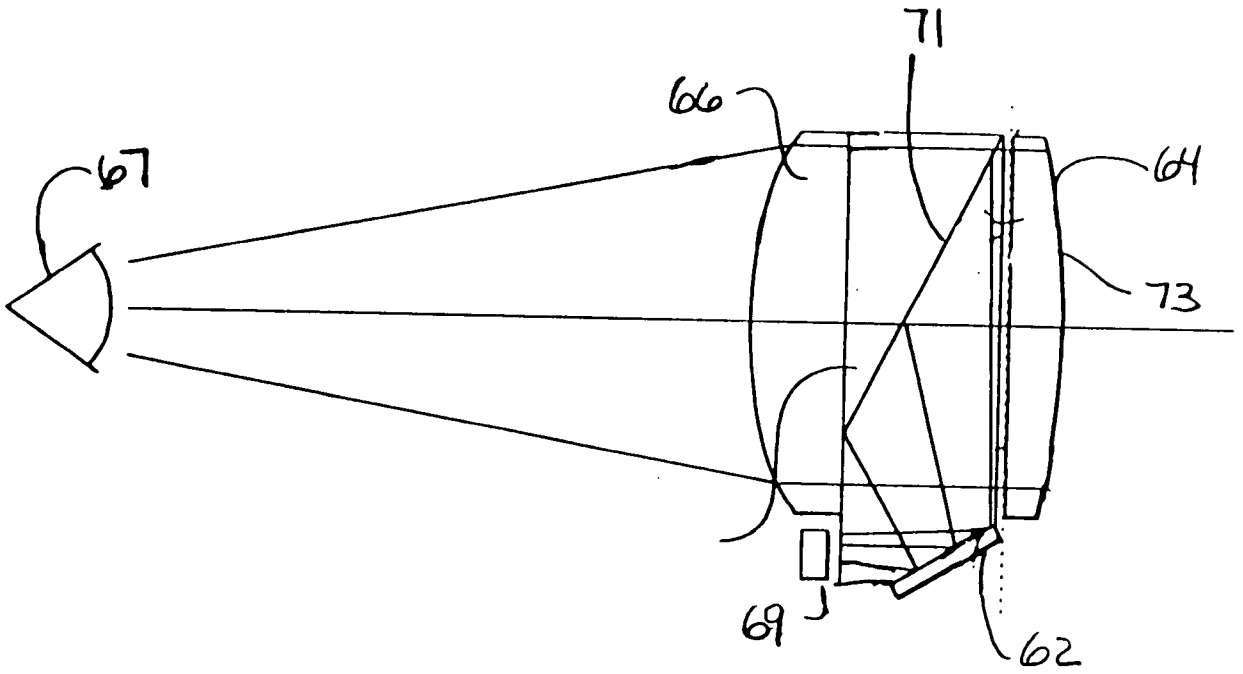
00h72F 8Fh2E260

FIGURE 7A



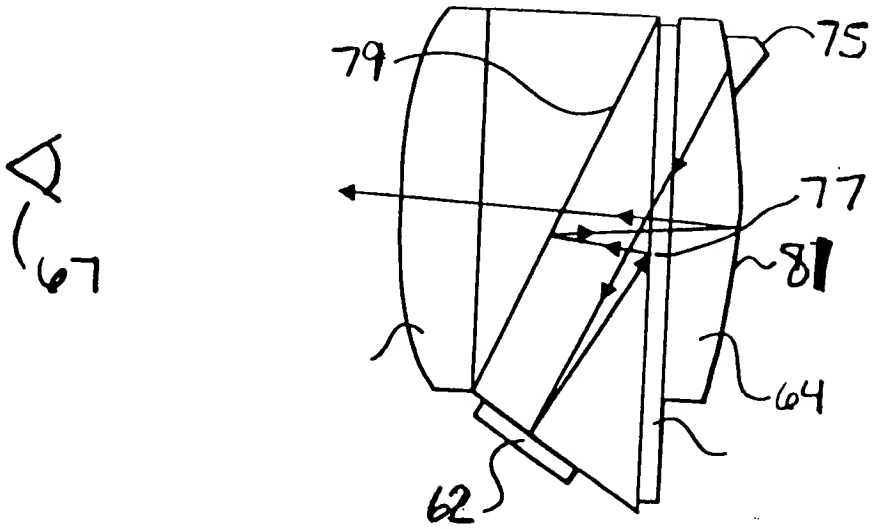
09737 8th Floor

FIGURE 7B



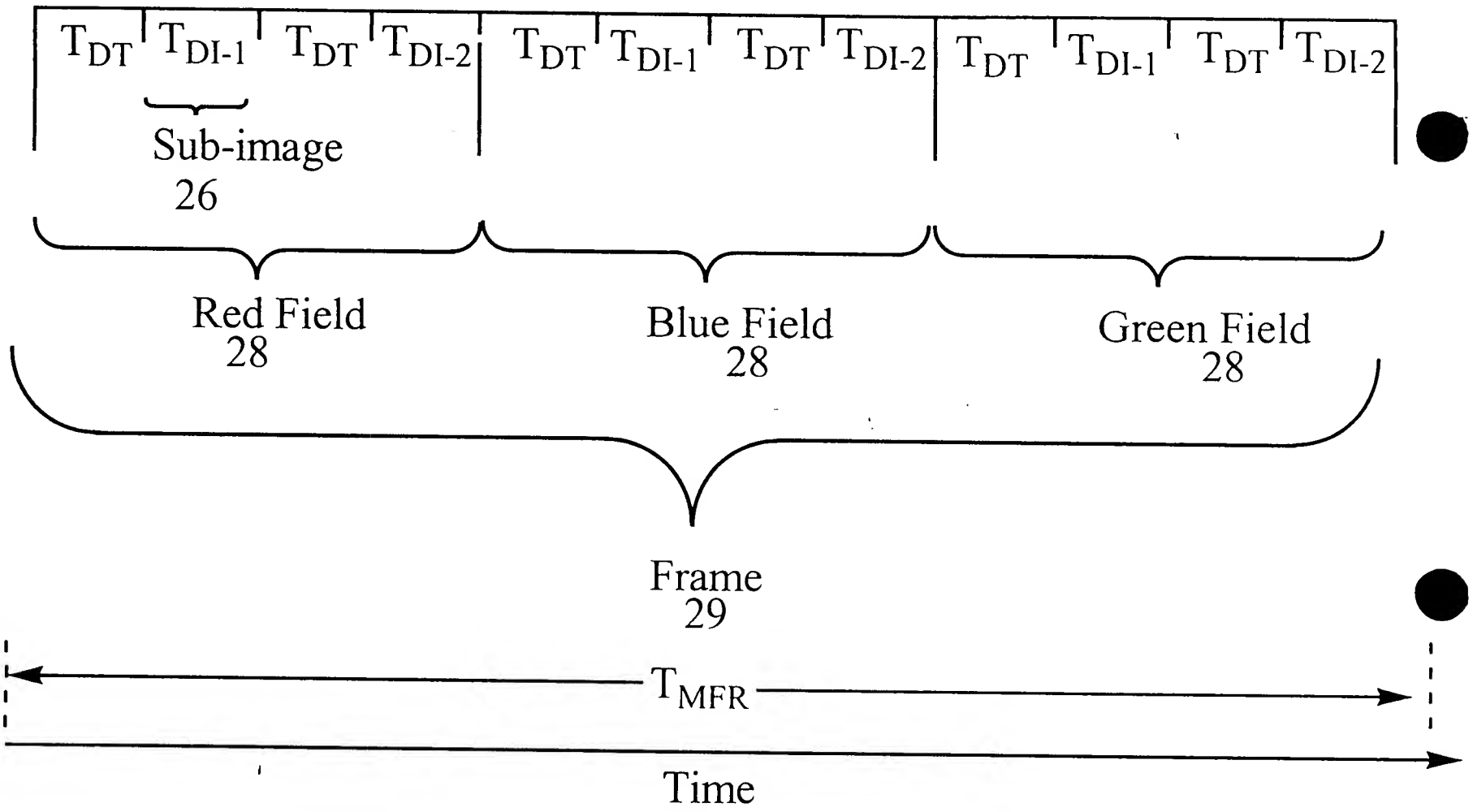
DOCKET ALARM

FIGURE 7C



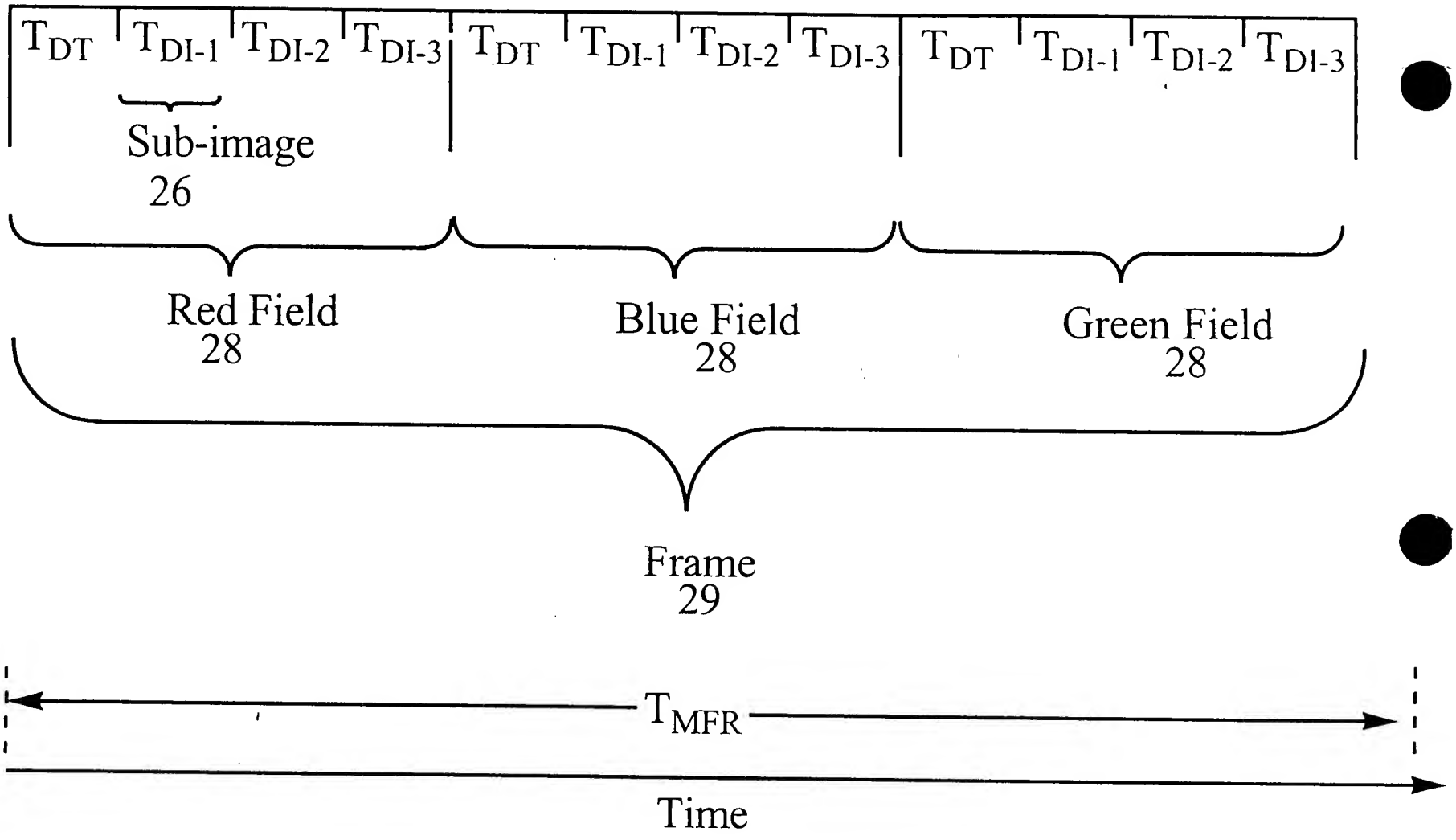
09h12T" 8Th2E260

FIGURE 8A



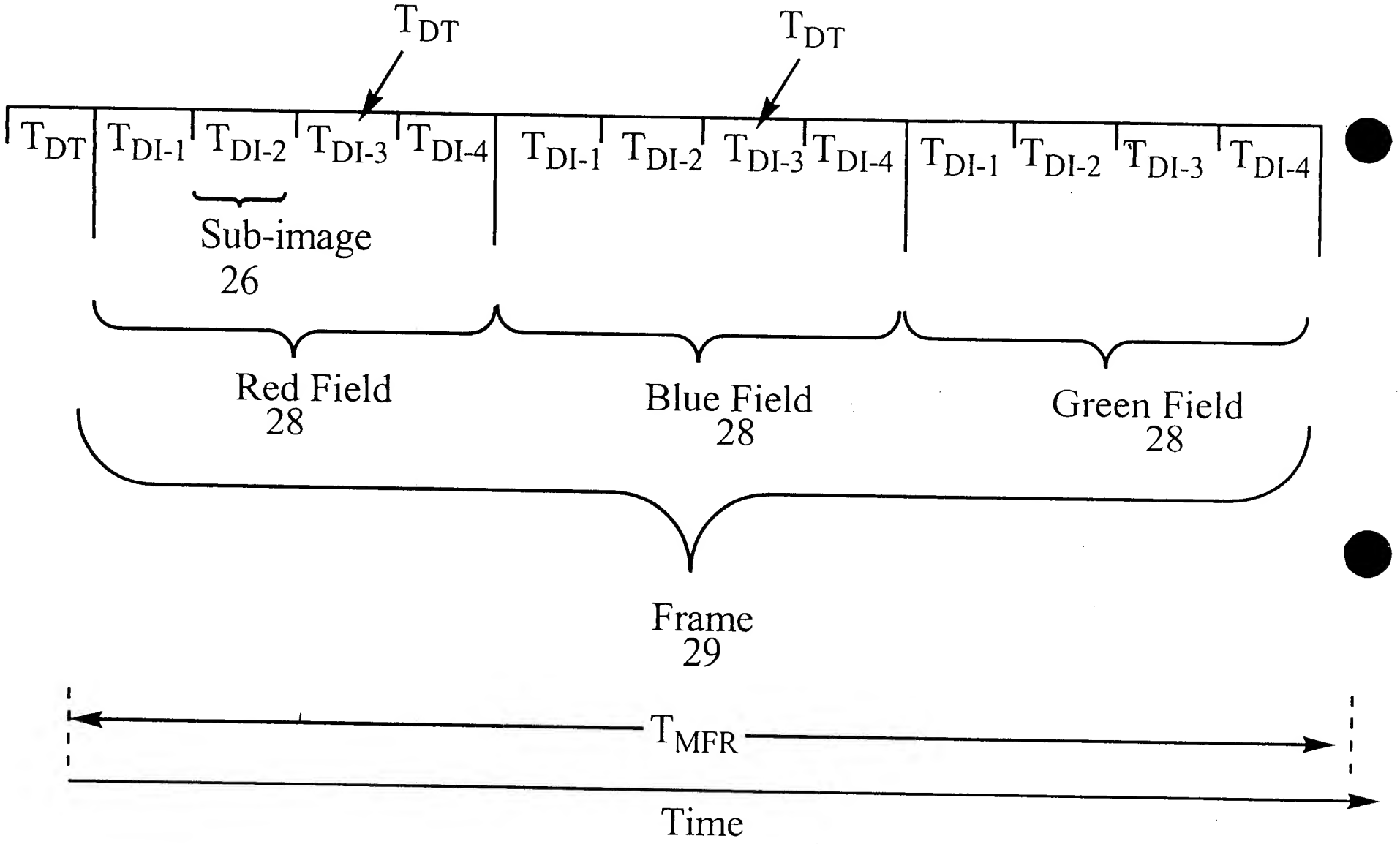
00T2F 3Th2E260

FIGURE 8B



DO NOT PRINT

FIGURE 8C



00nT2T' BTn7E760

FIGURE 9A

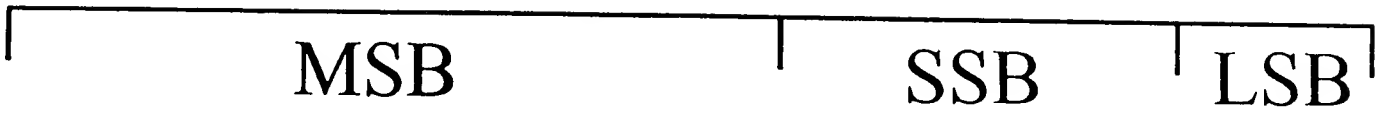


FIGURE 9B

MSB most significant bit
SSB second significant bit
LSB least significant bit

00T2T"8T4ZE760

FIGURE 10

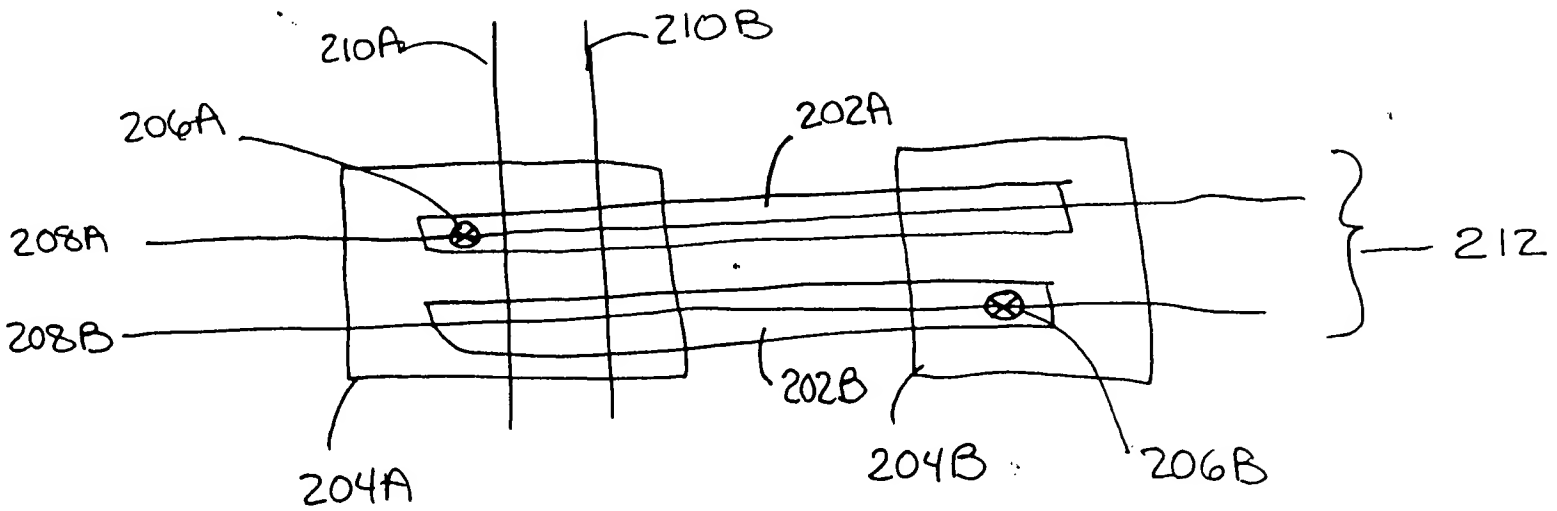
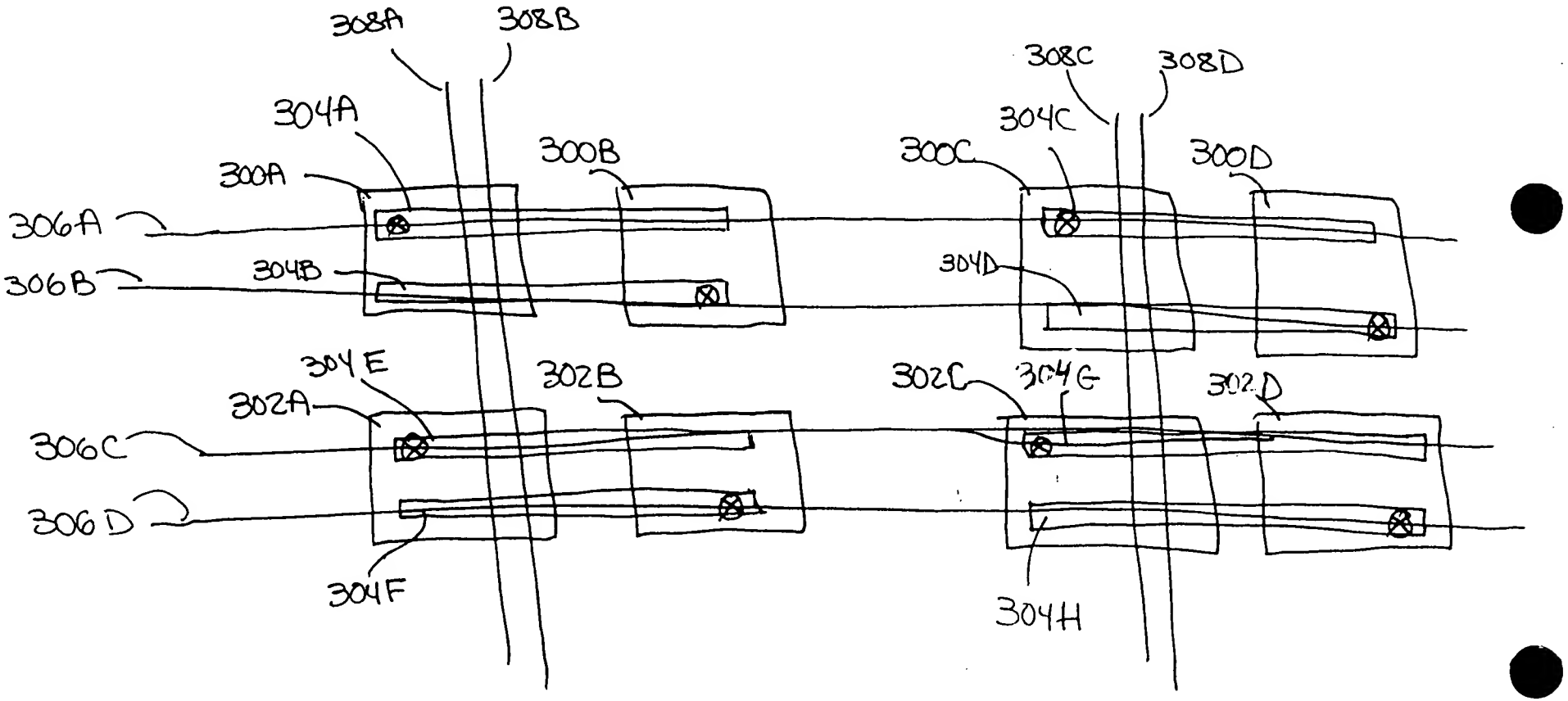


FIGURE 11


308A
308B
308C
308D
304A
304C
300A
300B
300C
300D
306A
306B
304B
304D
304E
302B
302A
302C
304G
302D
306C
306D
304F
304H

Omit "Bth2E260

FIGURE 12

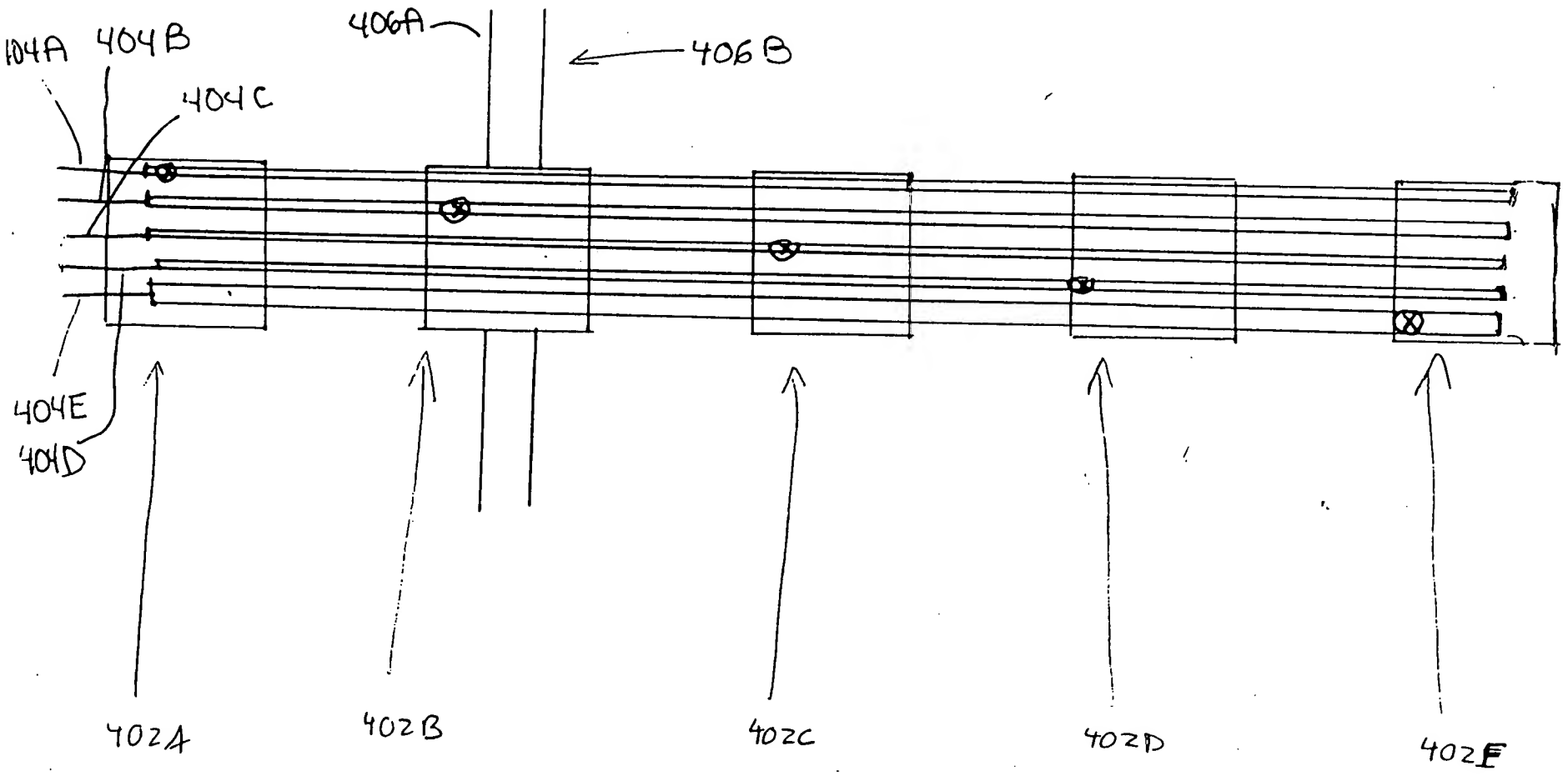


FIGURE 13

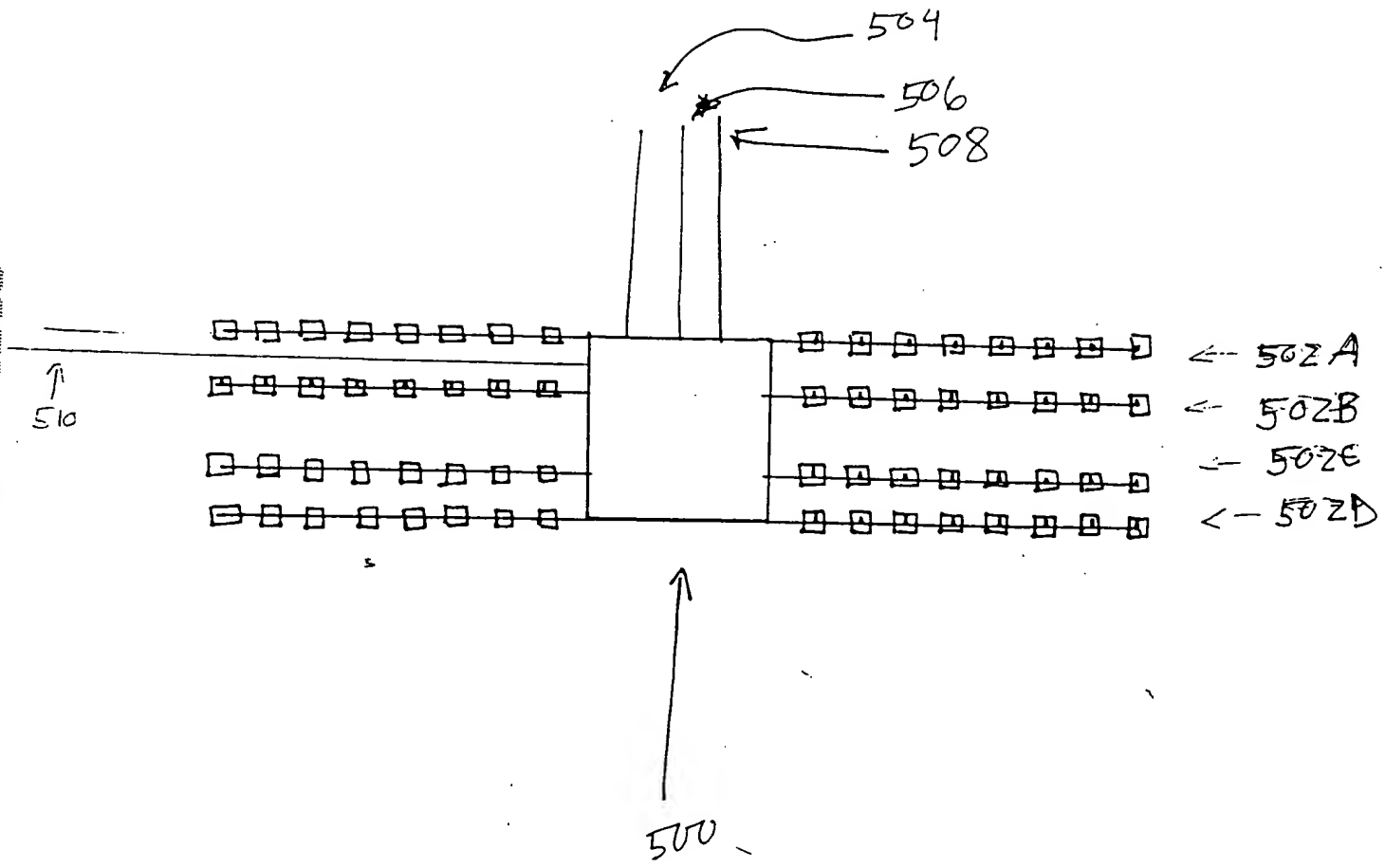


FIGURE 14

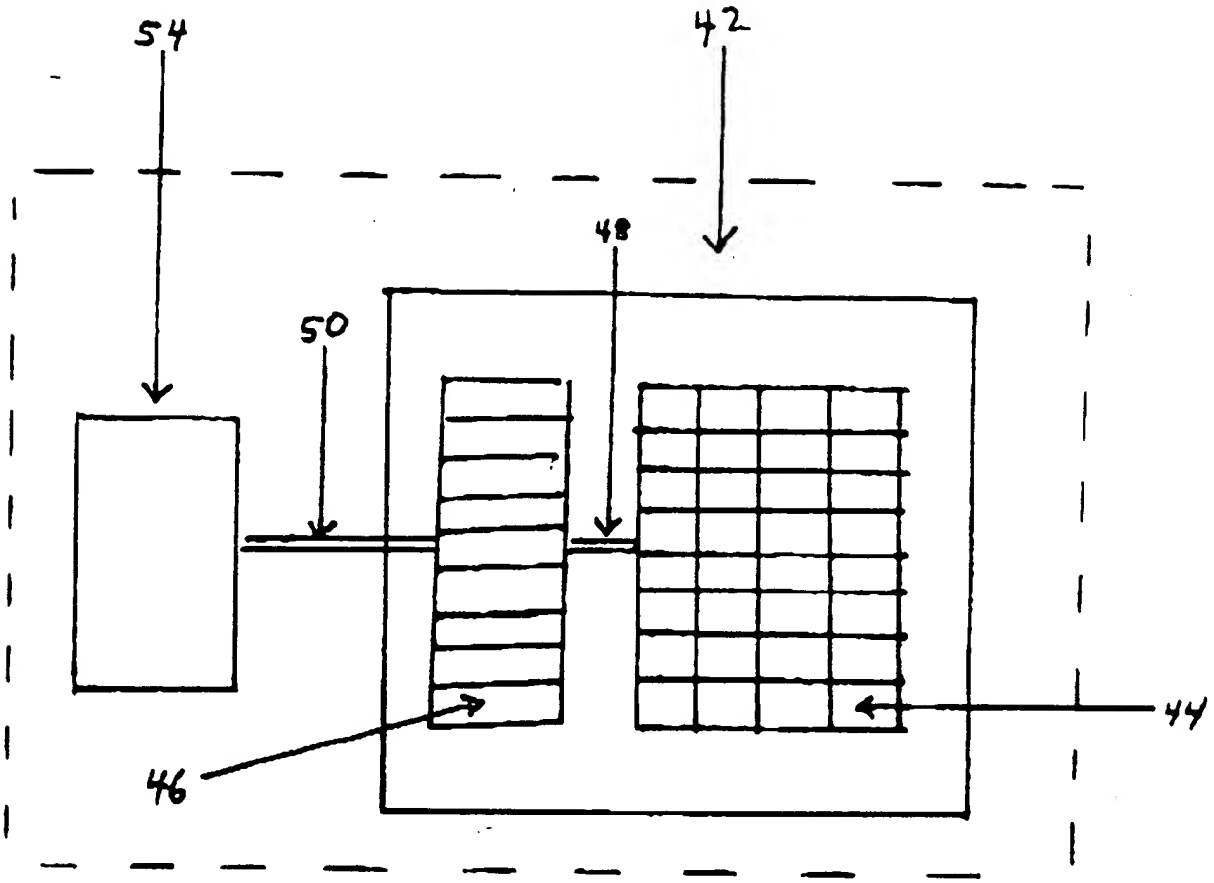
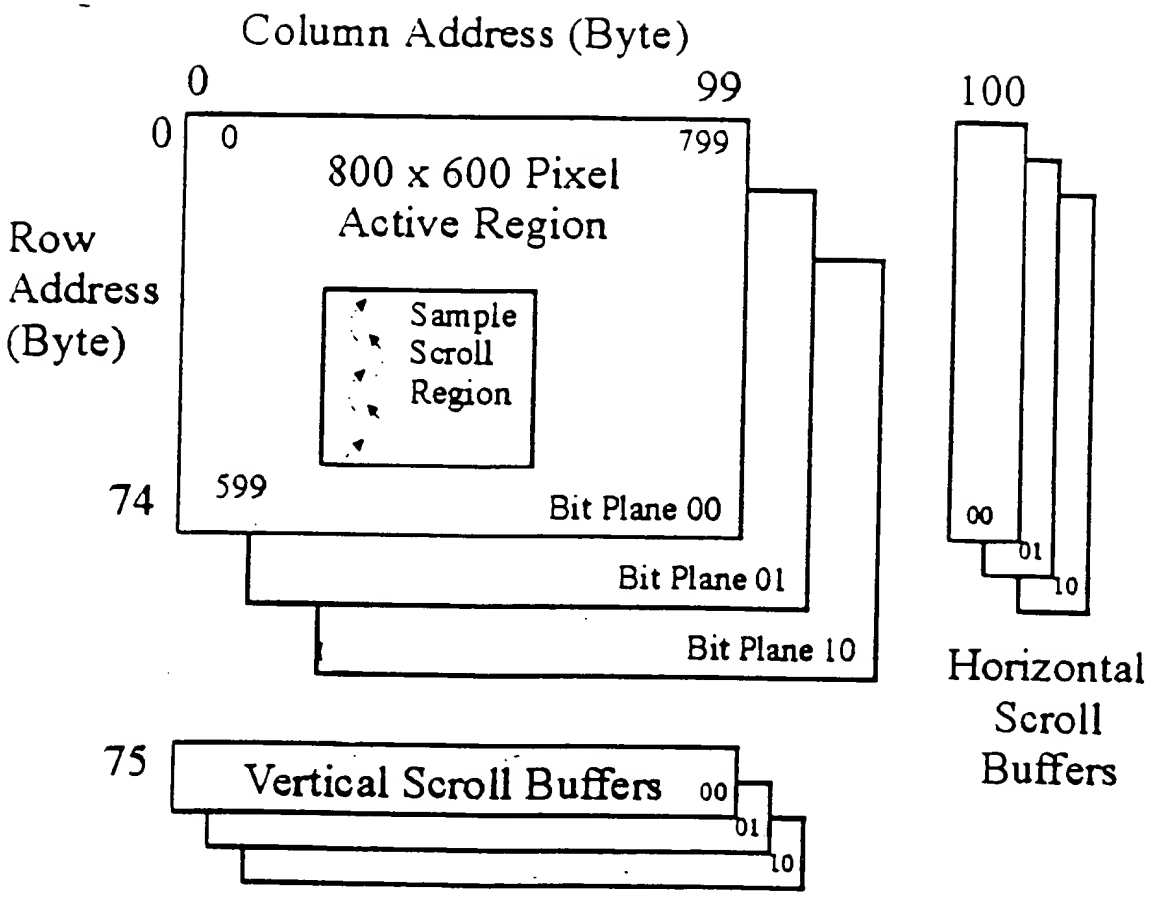


FIGURE 15A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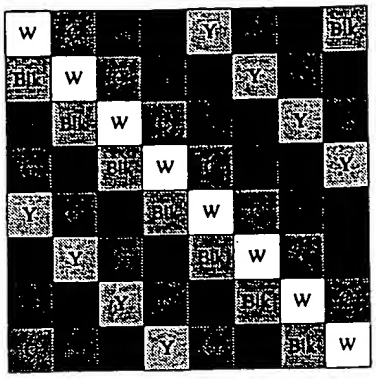
100
0
0
799
800 x 600 Pixel
Active Region
Row
Address
(Byte)
Sample
Scroll
Region
74
599
Bit Plane 00
Bit Plane 01
Bit Plane 10
00
01
10
Horizontal
Scroll
Buffers
75
Vertical Scroll Buffers
00
01
10

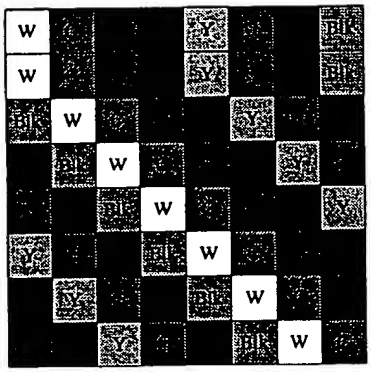


Original Scroll Region

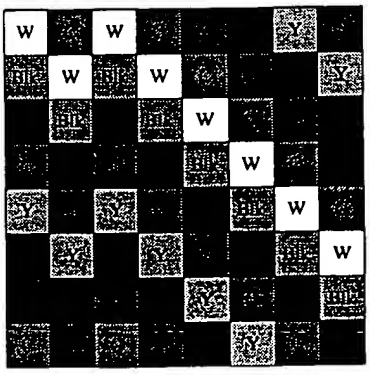
Key

B	G	R	Color
0	0	0	White
0	0	1	Cyan
0	1	0	Magenta
0	1	1	Blue
1	0	0	Yellow
1	0	1	Green
1	1	0	Red
1	1	1	Black

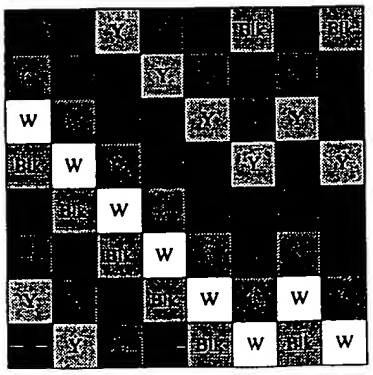
Pixel



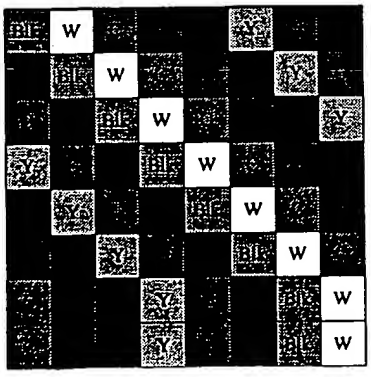
Scroll Up



Scroll Left



Scroll Right



Scroll Down

FIGURE 15B

Docket: 3564.003

FIGURE 16

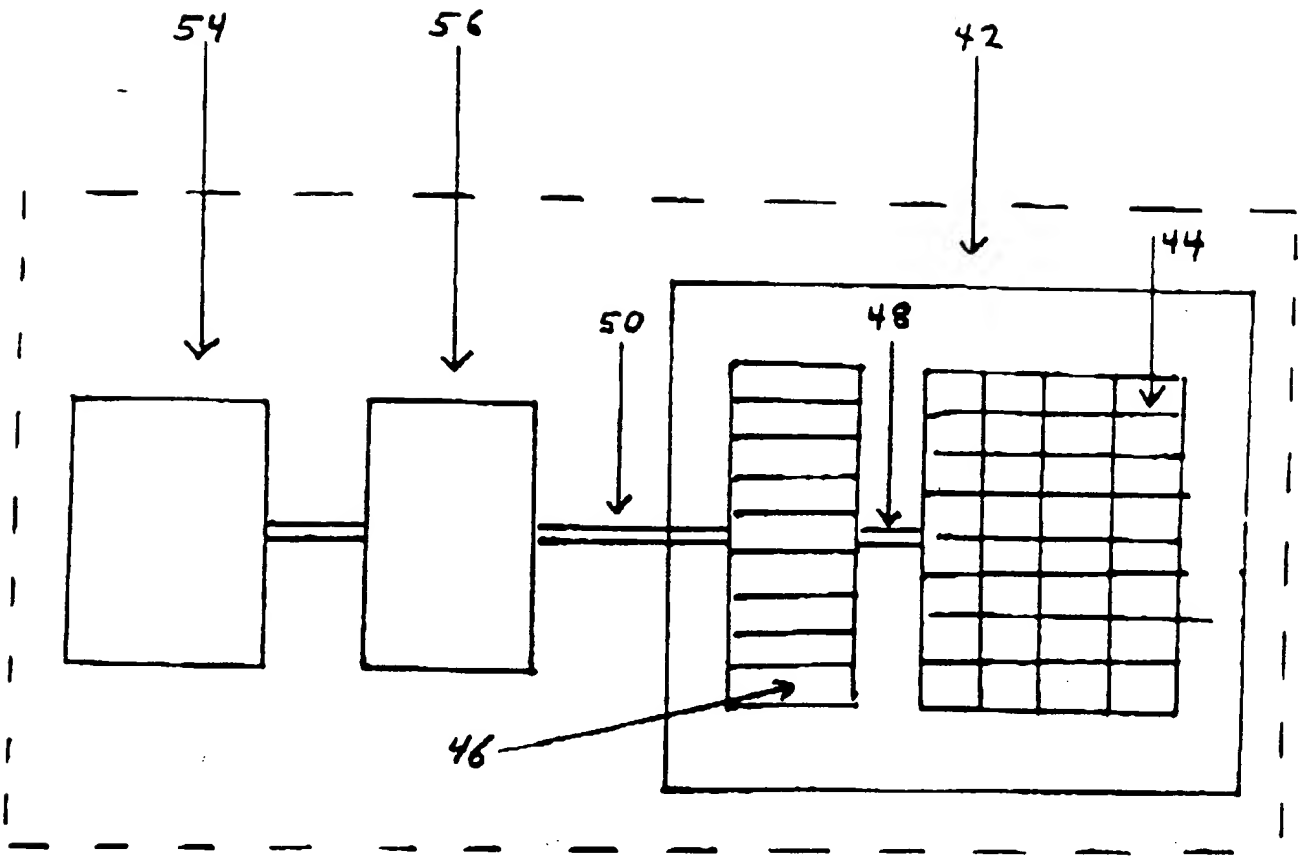
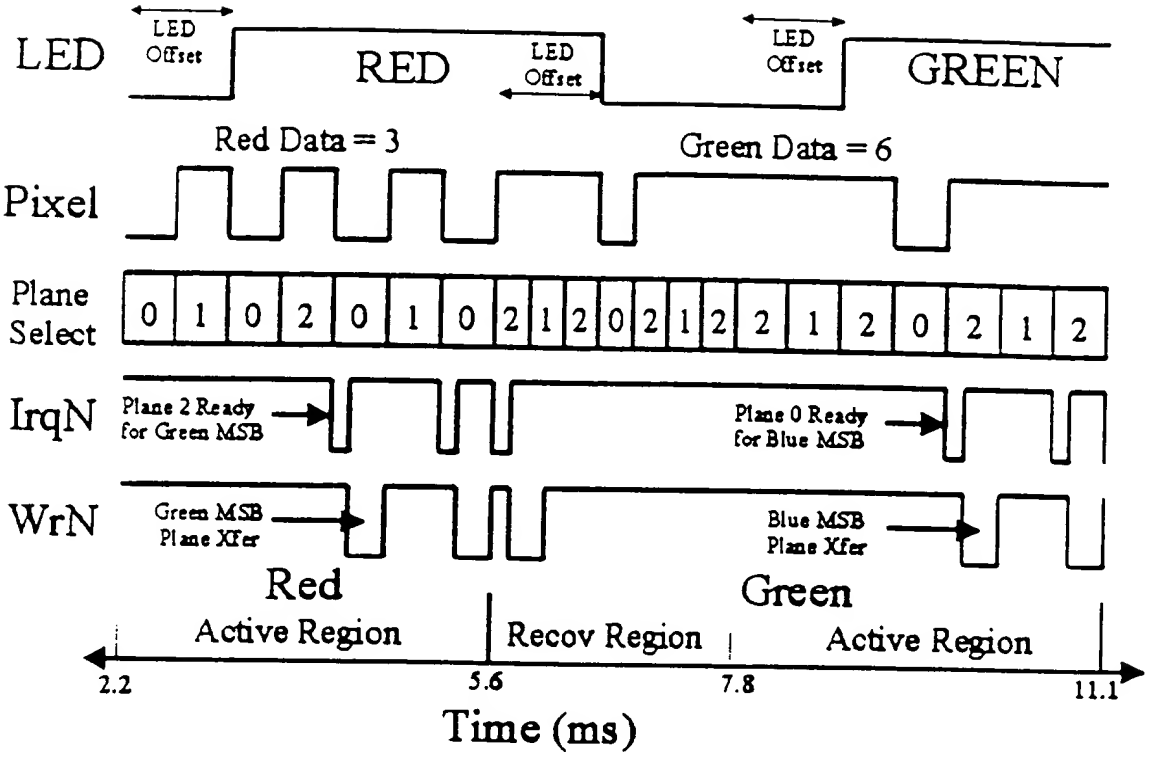
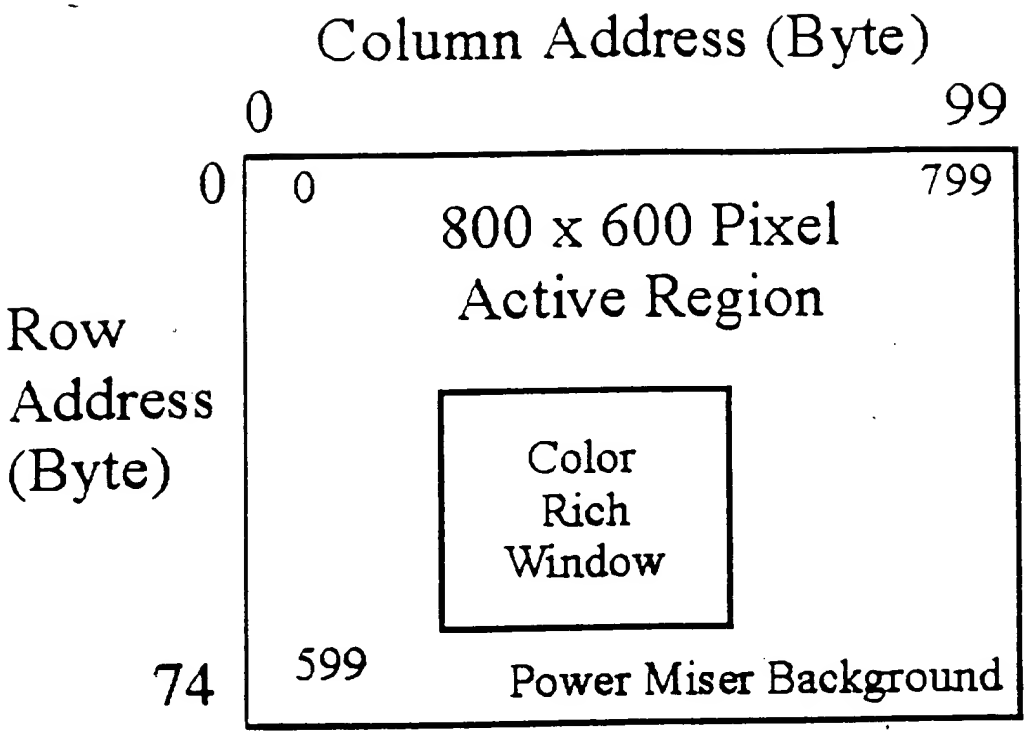


FIGURE 17



Doc#: 3-689-1-2-0-4-0-0

FIGURE 18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Row
Address
(Byte)
Color
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Window
74
599
Power Miser Background

00nF2F" 8Fn4E260

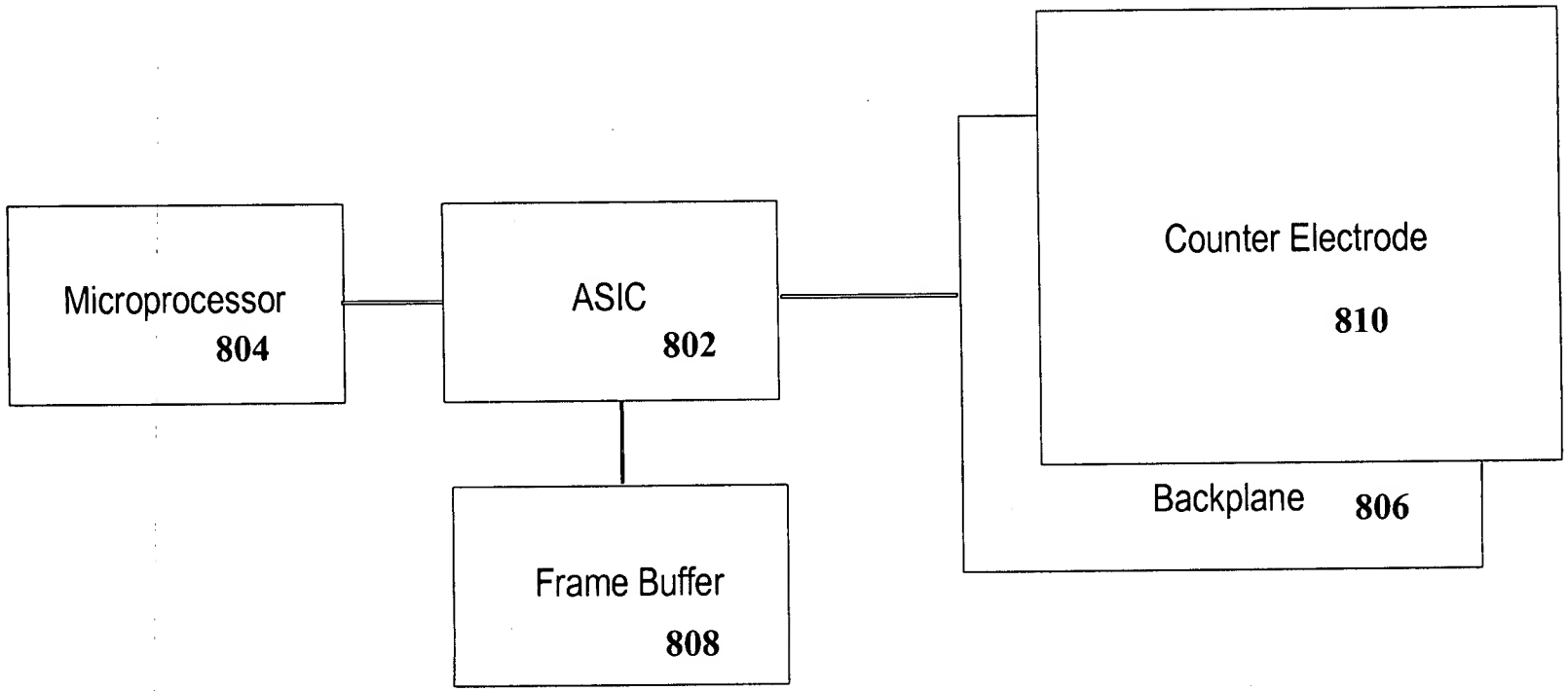

Fig. 19

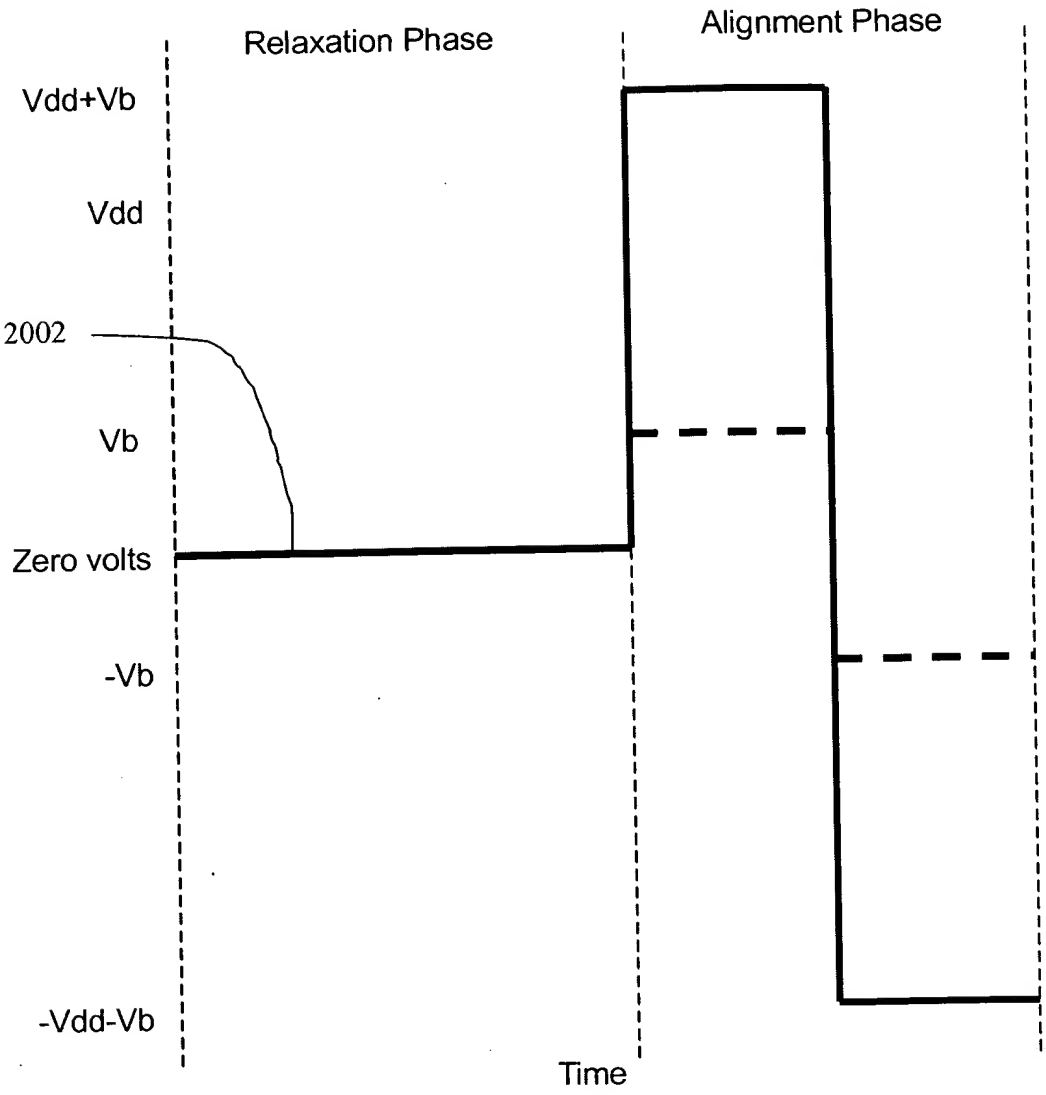
Relaxation Phase
Alignment Phase
Vdd+Vb
Vdd
2002
Vb
Zero volts
-Vb
-Vdd-Vb
Time

Fig. 20

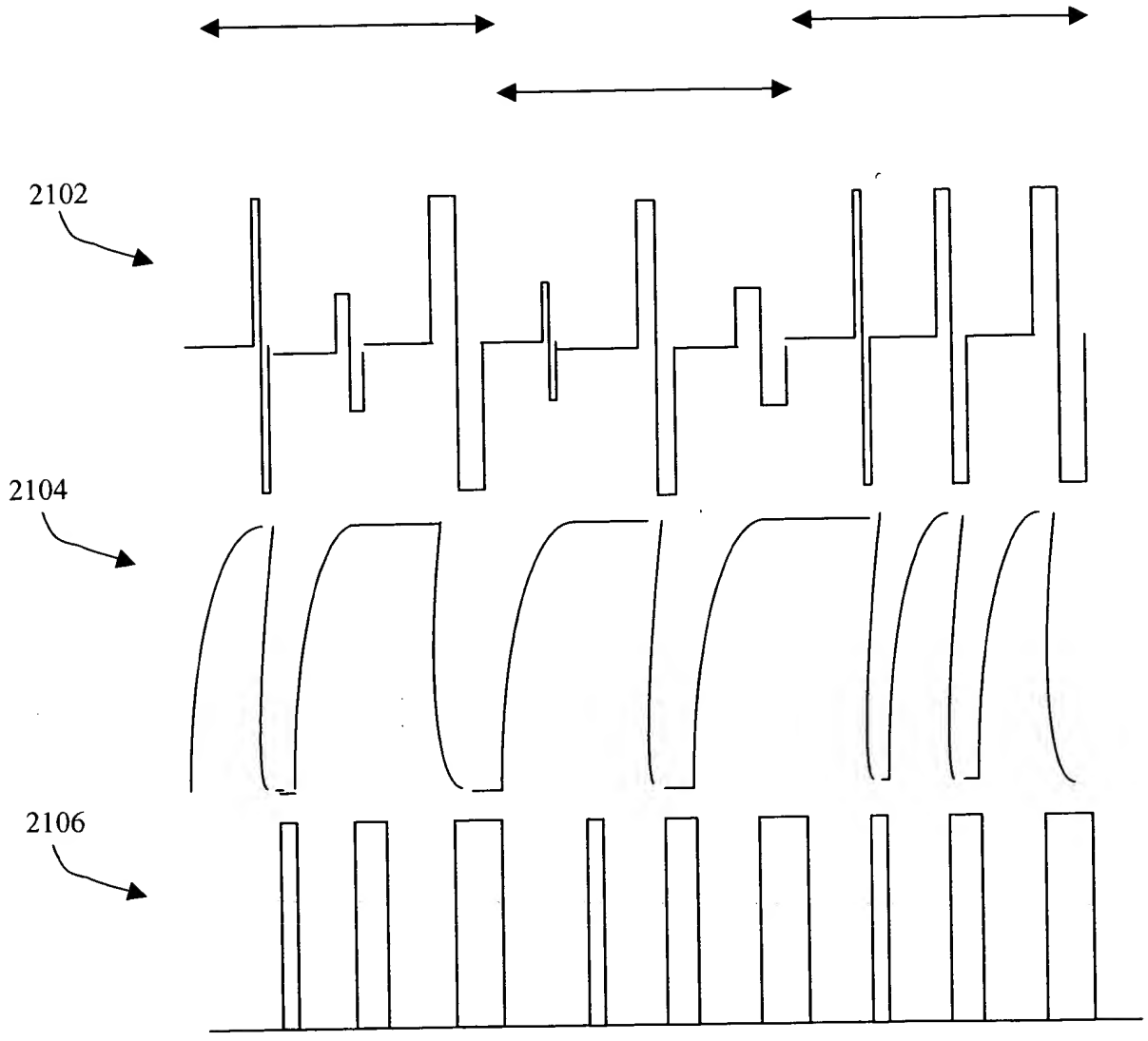

Fig. 21

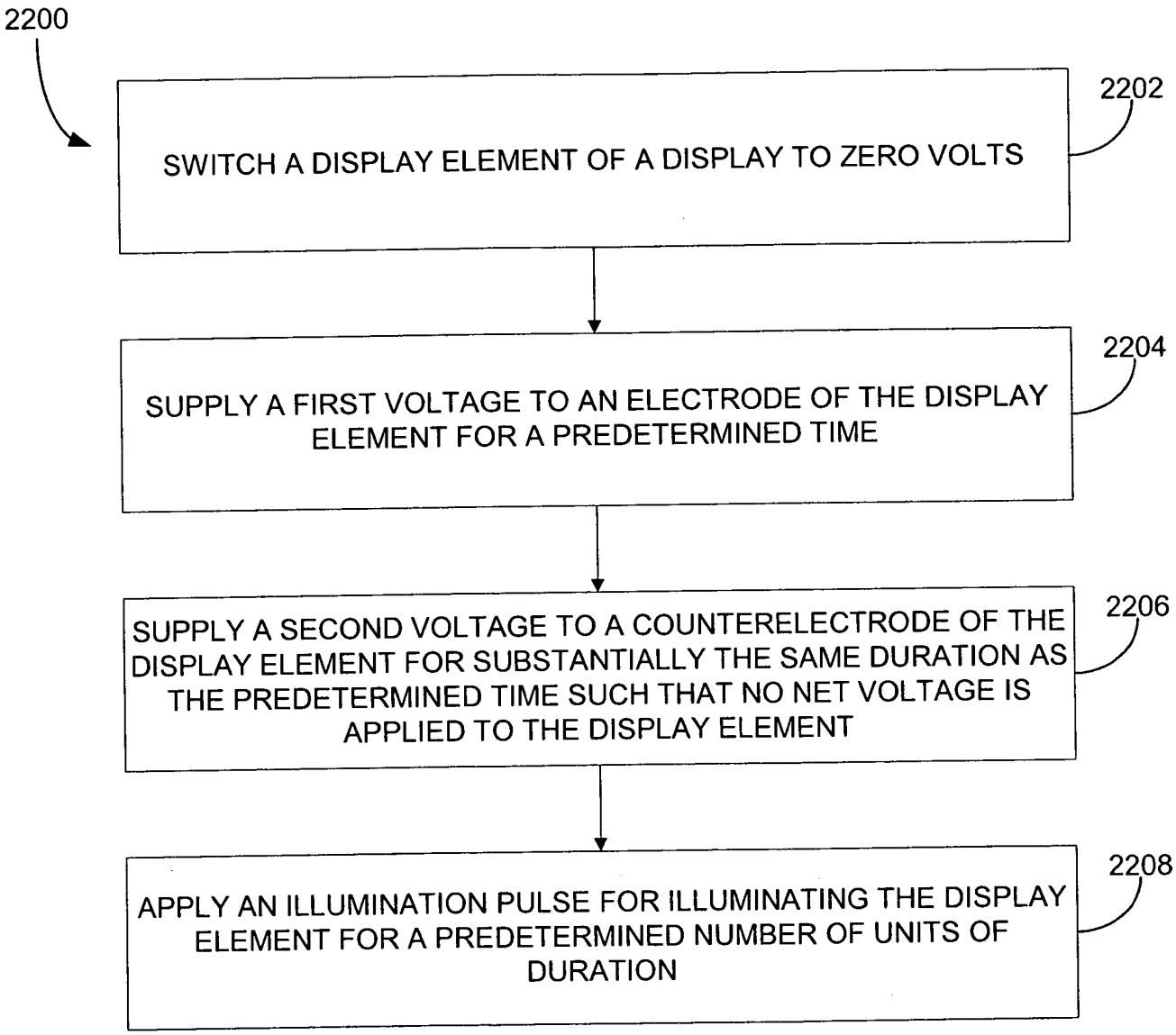
2200
SWITCH A DISPLAY ELEMENT OF A DISPLAY TO ZERO VOLTS
2202
SUPPLY A FIRST VOLTAGE TO AN ELECTRODE OF THE DISPLAY ELEMENT FOR A PREDETERMINED TIME
2204
SUPPLY A SECOND VOLTAGE TO A COUNTERELECTRODE OF THE DISPLAY ELEMENT FOR SUBSTANTIALLY THE SAME DURATION AS THE PREDETERMINED TIME SUCH THAT NO NET VOLTAGE IS APPLIED TO THE DISPLAY ELEMENT
2206
APPLY AN ILLUMINATION PULSE FOR ILLUMINATING THE DISPLAY ELEMENT FOR A PREDETERMINED NUMBER OF UNITS OF DURATION
2208

Fig. 22

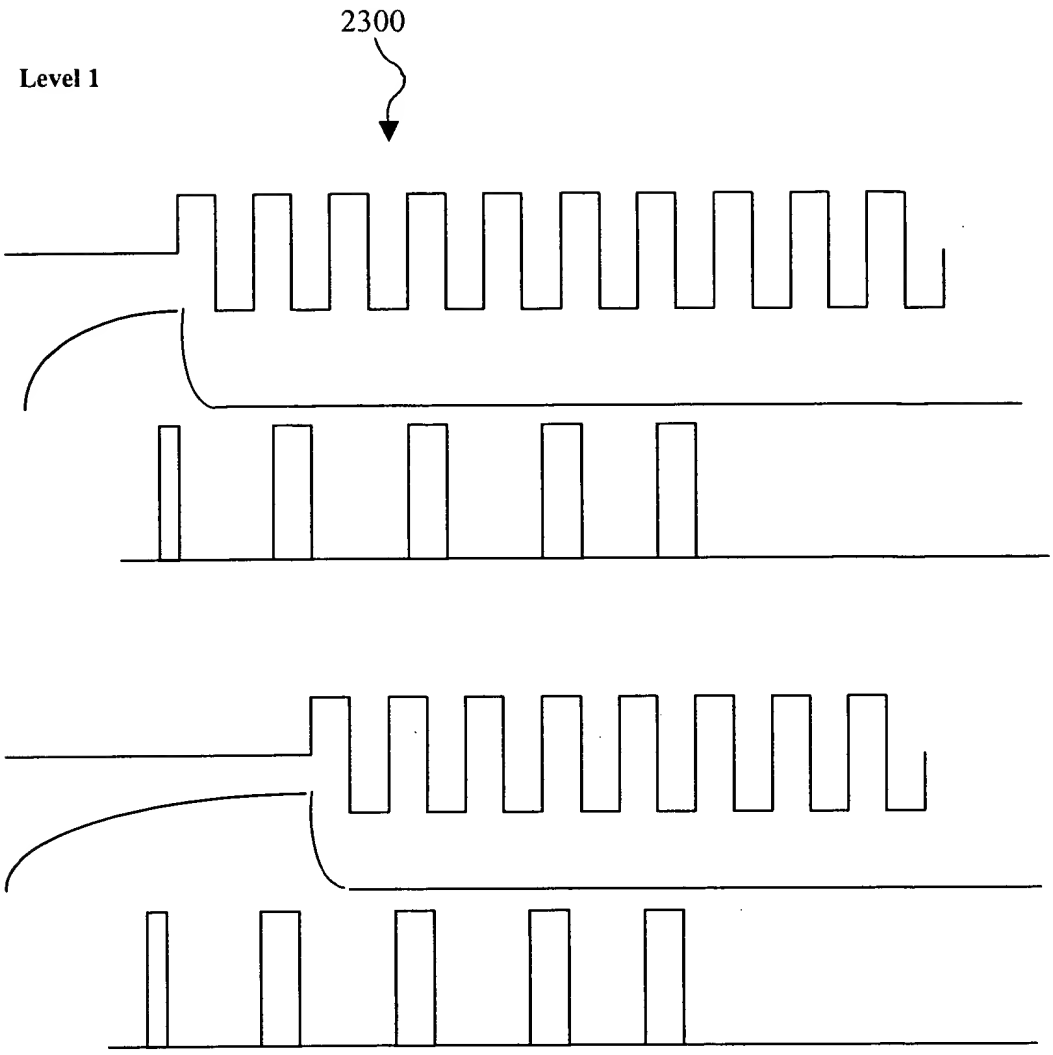

Fig. 23

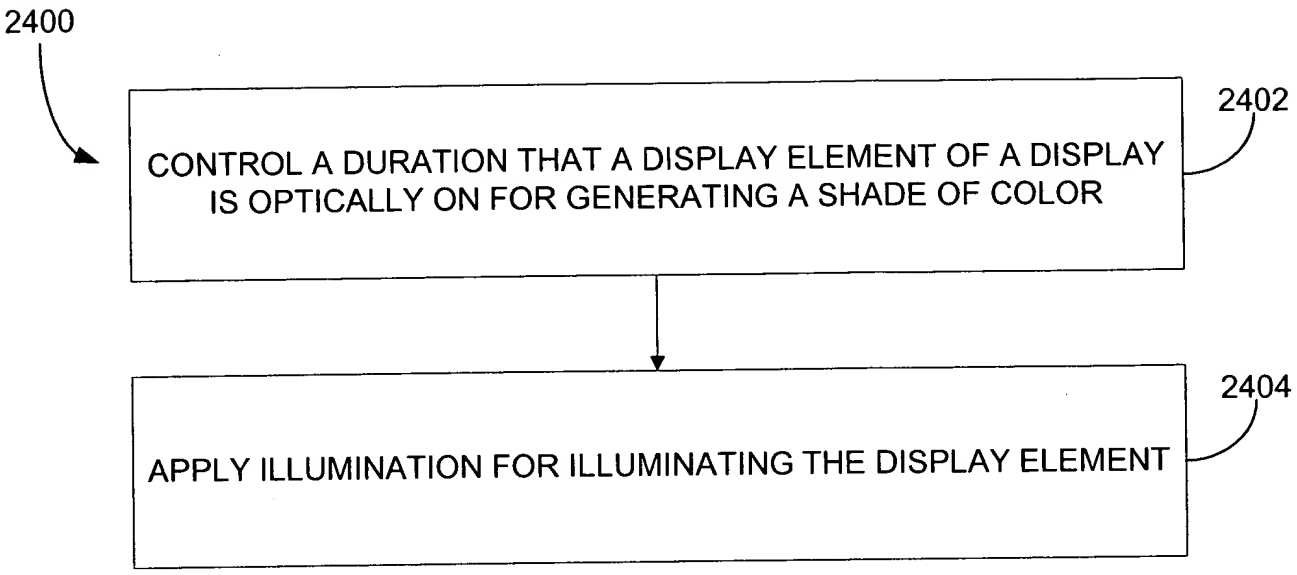
2400
CONTROL A DURATION THAT A DISPLAY ELEMENT OF A DISPLAY IS OPTICALLY ON FOR GENERATING A SHADE OF COLOR
2402
APPLY ILLUMINATION FOR ILLUMINATING THE DISPLAY ELEMENT
2404

Fig. 24

00h127"8Th7E760

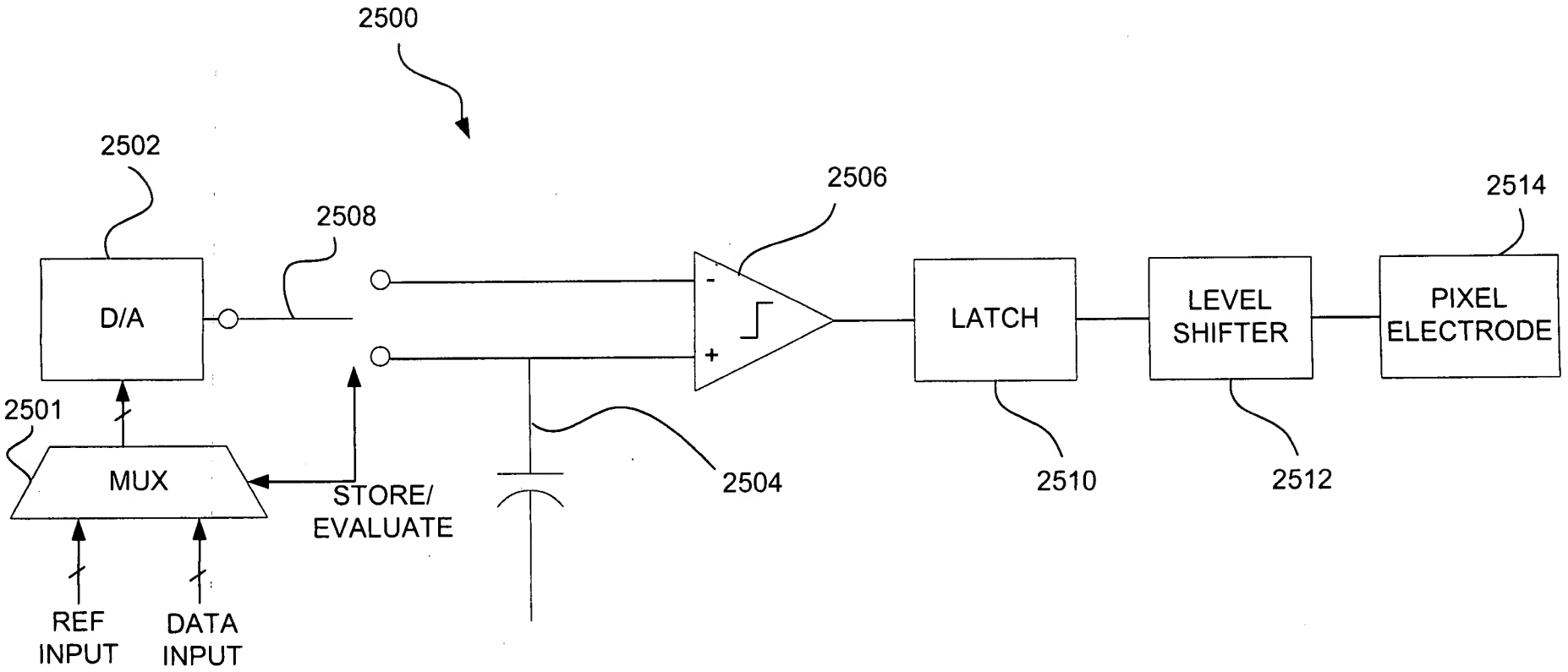

Fig. 25A

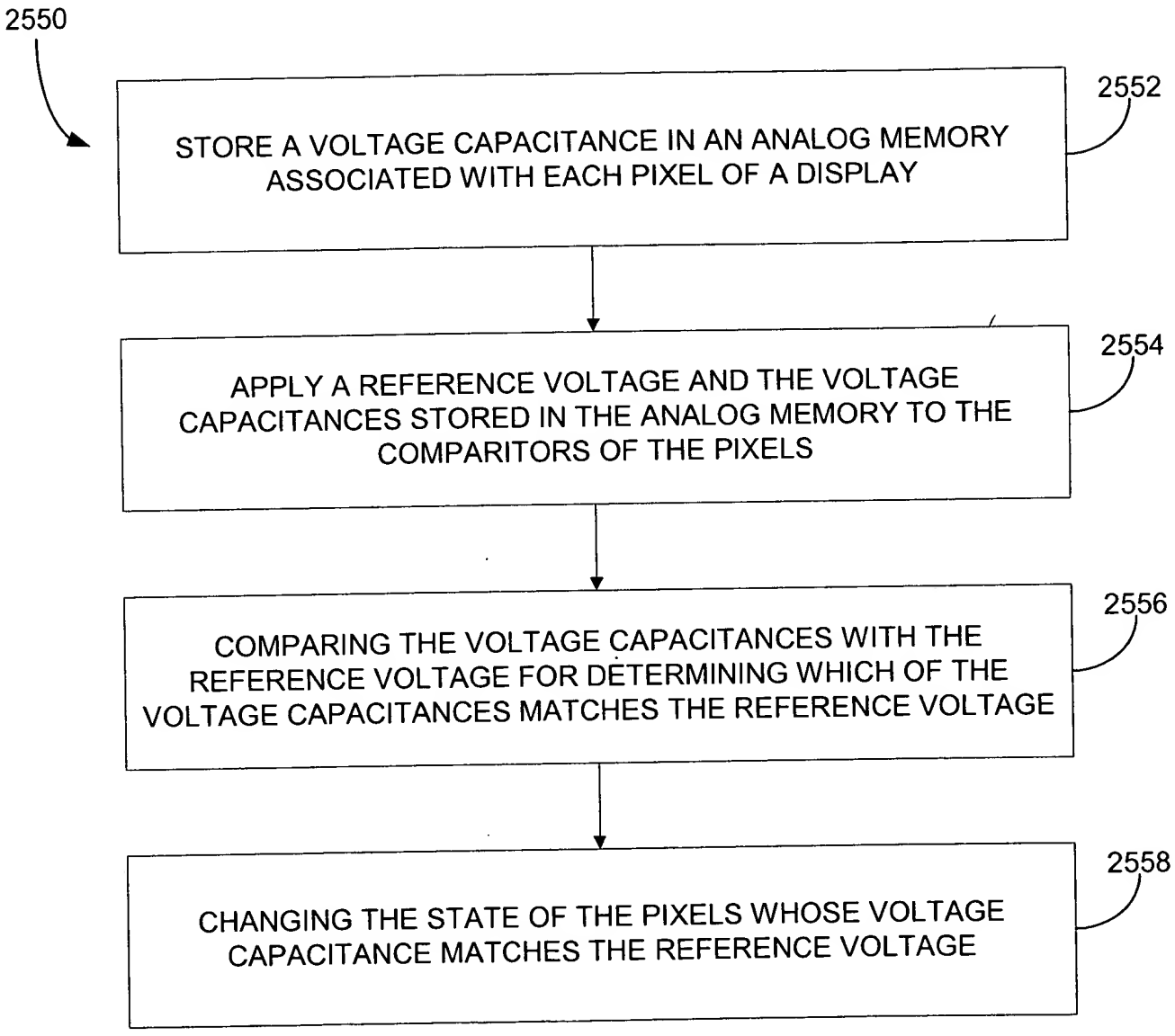
2550
STORE A VOLTAGE CAPACITANCE IN AN ANALOG MEMORY ASSOCIATED WITH EACH PIXEL OF A DISPLAY
2552
APPLY A REFERENCE VOLTAGE AND THE VOLTAGE CAPACITANCES STORED IN THE ANALOG MEMORY TO THE COMPARITORS OF THE PIXELS
2554
COMPARING THE VOLTAGE CAPACITANCES WITH THE REFERENCE VOLTAGE FOR DETERMINING WHICH OF THE VOLTAGE CAPACITANCES MATCHES THE REFERENCE VOLTAGE
2556
CHANGING THE STATE OF THE PIXELS WHOSE VOLTAGE CAPACITANCE MATCHES THE REFERENCE VOLTAGE
2558

Fig. 25B

Columns

Rows

A	B	A	B
B	A	B	A
A	B	A	B

Fig. 26

Columns

Rows

A	B	C	D	A	B	C	D
C	D	A	B	C	D	A	B
B	A	D	C	B	A	D	C
D	C	B	A	D	C	B	A
A	B	C	D	A	B	C	D
C	D	A	B	C	D	A	B

Fig. 27

09/534,409 - 11/04/05